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**RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2124
Docket No.: 1538.1039**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Shigeo ORII

Serial No. 10/619,462

Group Art Unit: 2124

Confirmation No. 7714

Filed: July 16, 2003

Examiner:

For: PARALLEL EFFICIENCY CALCULATION METHOD AND APPARATUS

COMMUNICATION TO THE EXAMINER

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

A copy of the Information Disclosure Statement filed on July 16, 2003 in the above-referenced application is attached. The Examiner has not returned a copy of Attachment 1(f), indicating the reference is considered.

It is respectfully requested that the Examiner return a copy of the Attachment 1(f) accompanying the Information Disclosure Statement filed July 16, 2003, signed and initialed to indicate that the reference AG listed therein also was considered by the Examiner.

Since the application has been allowed, this matter is somewhat urgent.

Respectfully submitted,

STAAS & HALSEY LLP

Date: 6 - 8 - 09

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EARLY SERIAL NUMBER CARD

COPY

Please Date Stamp and return

PATENT APPLICATION w/29 SHEETS OF DRAWINGS, DECLARATION, ASSIGNMENT, PRIORITY DOCUMENT, IDS, and check for \$2,050.00

APPLICANT(S): Shigeo ORII

SERIAL NO:

CONFIRMATION NO.

TITLE: PARALLEL EFFICIENCY CALCULATION METHOD AND APPARATUS

FILING DATE: July 16, 2003

DOCKET NO: 1538.1039/HJS:sjt

DUE DATE: July 22, 2003



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